PATENT	APPLICATION	SERIAL	NO.	

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE FEE RECORD SHEET

04/23/2004 GWORDOF1 00000029 090458 10709222

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PTO-1556 (5/87)

Electronic Filing System (EFS) Data Electronic Patent Application Submission USPTO Use Only

EFS ID:

59476

Application ID:

10709222

STRUCTURE AND METHOD OF

FORMING A BIPOLAR

TRANSISTOR HAVING A SELF-

Title of Invention:

ALIGNED RAISED EXTRINSIC BASE

USING A LINK-UP REGION FORMED FROMAN OPENING

THEREIN

First Named Inventor:

Marwan Khater

Domestic/Foreign Application:

Domestic Application

Filing Date:

2004-04-22

Effective Receipt Date:

2004-04-22

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Utility Patent Filing

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3221

Attorney Docket Number:

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Deposit Account Number:

90458

Deposit Account Name:

H. Daniel Schnurmann

RAM Payment Status:

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Digital Certificate Holder: cn=H. Daniel Schnurmann,ou=Registered Attorneys,ou=Patent and

Trademark Office,ou=Department of Commerce,o=U.S. Government,c=US Certificate Message Digest: 49977862503c0a38dfa6069fba2715ee6171f207

PATENT APPLICATION FEE DETERMINATION RECORD

Effective October 1, 2003

Application or Docket Number

10709222

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